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STATEMENT BY APPLICANT	Applicant(s)	Shougo Imada, et al
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U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS					
EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶
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OTHER DOCUMENTS			
EXAMINER INITIALS	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		Japan Office action dated February 5, 2008, for priority Japan Application 2003-024733, with English translation indicating relevance of references listed in an IDS filed on April 4, 2008.	
		English translation of part of page 146 for reference: "Mechanism for delivering a plug and play resource from a BIOS to an OS", WinPC Labs, Nikkei WinPC, (09/2002), pp 146 to 147, previously filed April 4, 2008.	

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